

JFW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
 Kenji YONEDA : Confirmation Number: 9901
 Application No.: 10/774,522 : Group Art Unit: 2891
 Filed: February 10, 2004 : Examiner: SARKAR, Asok K.
 :
 For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Restriction Requirement
 Honorable Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

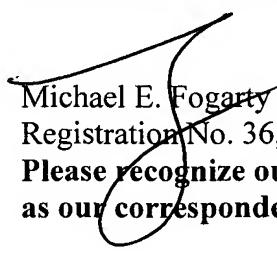
Sir:

Noting the Office Action of June 23, 2005 wherein restriction has been required, Applicant hereby elects Group I (claims 1-19) drawn to a method for fabricating a semiconductor device, and further Species I (Embodiment 1), upon which claims 1-3 and 5-19 are readable, for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


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